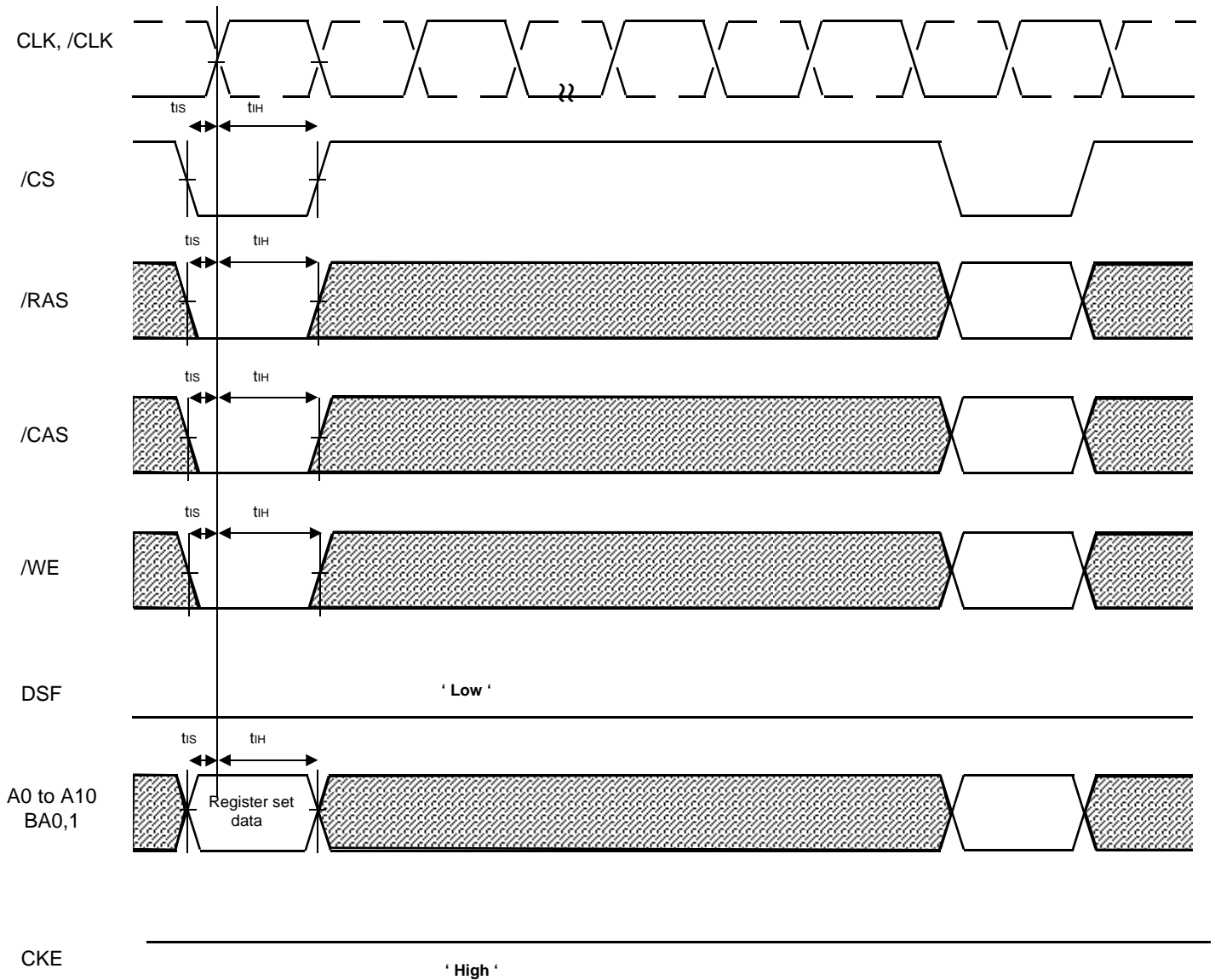
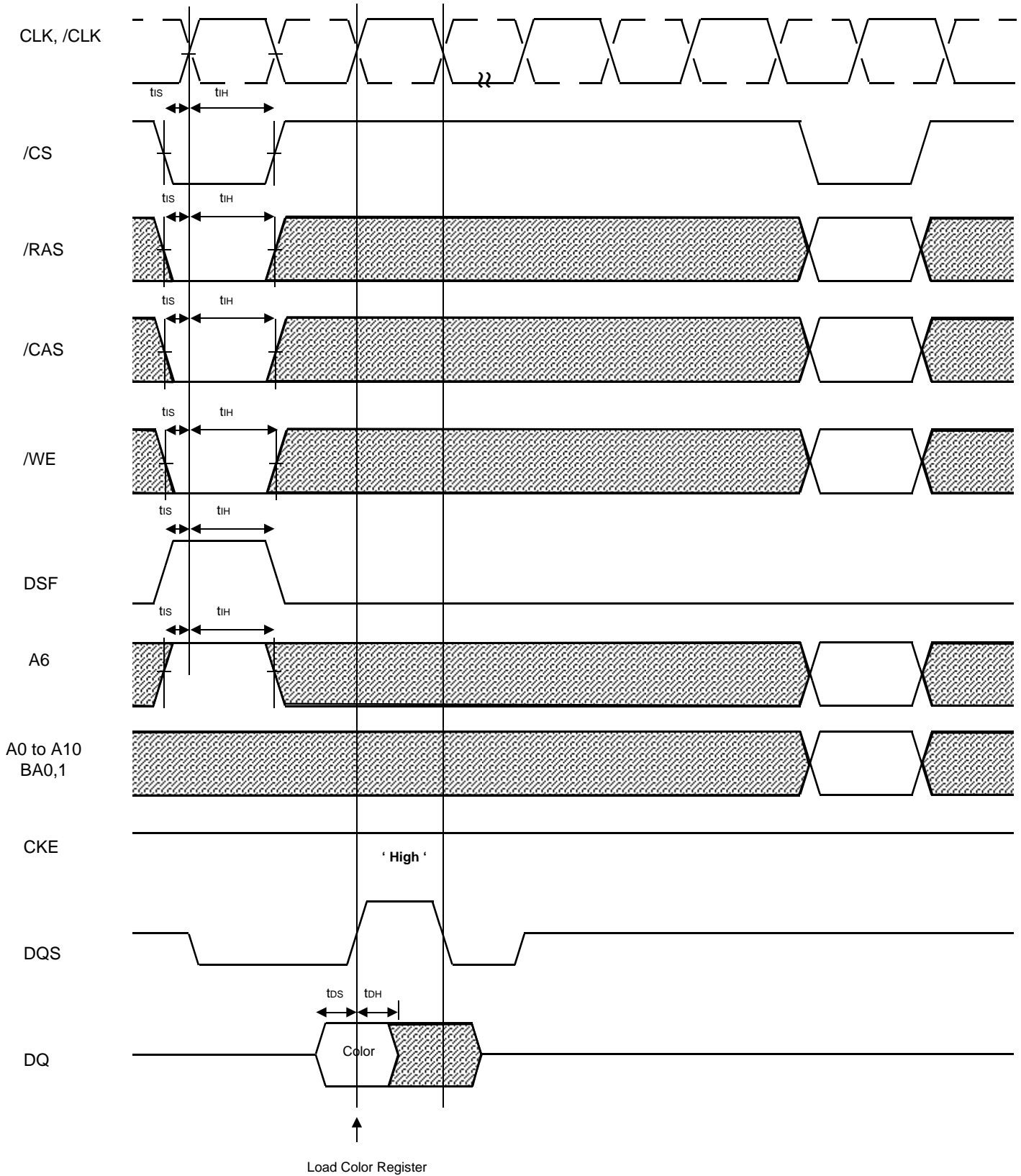


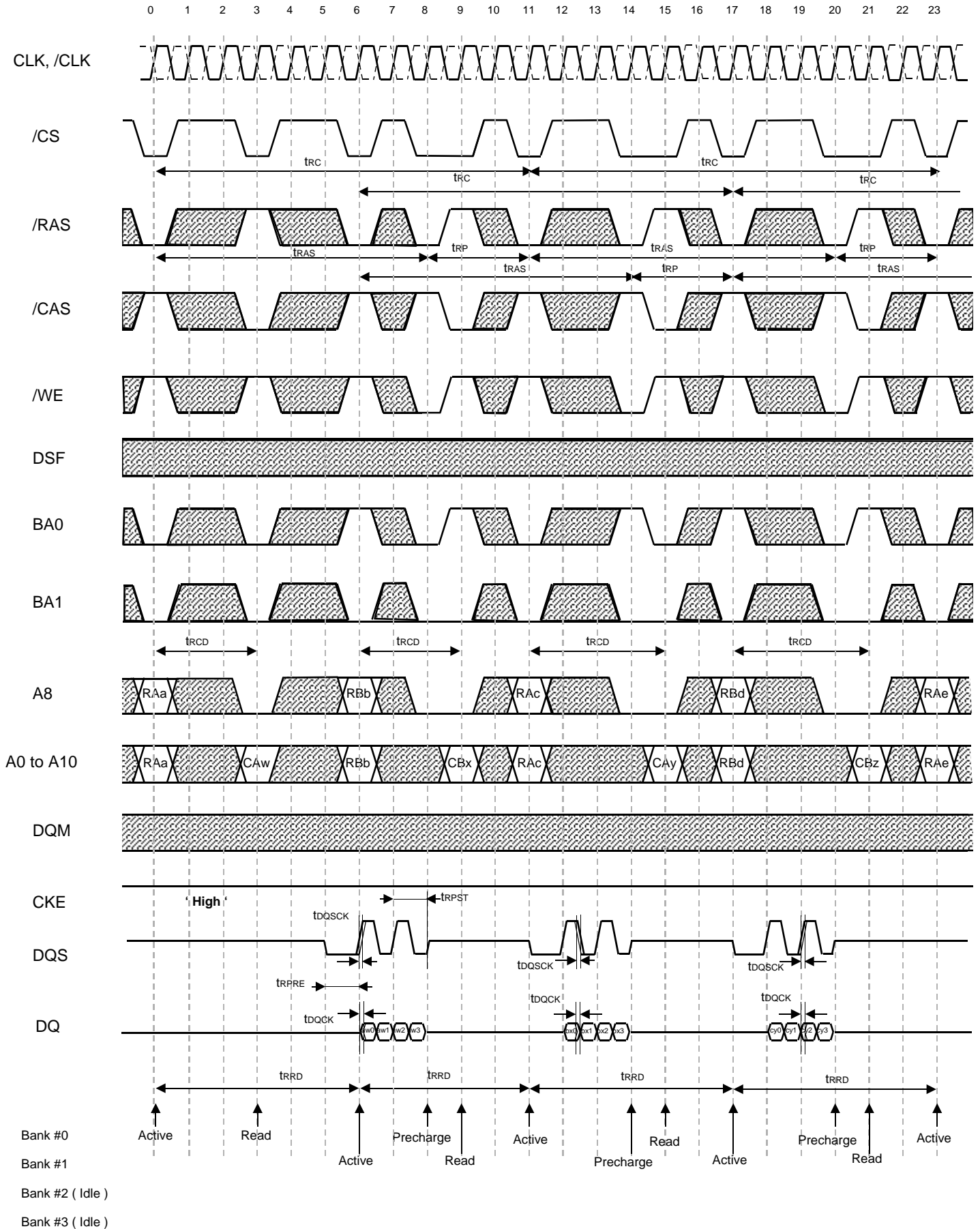
**Mode Register and Extended Mode Register Set Cycle**



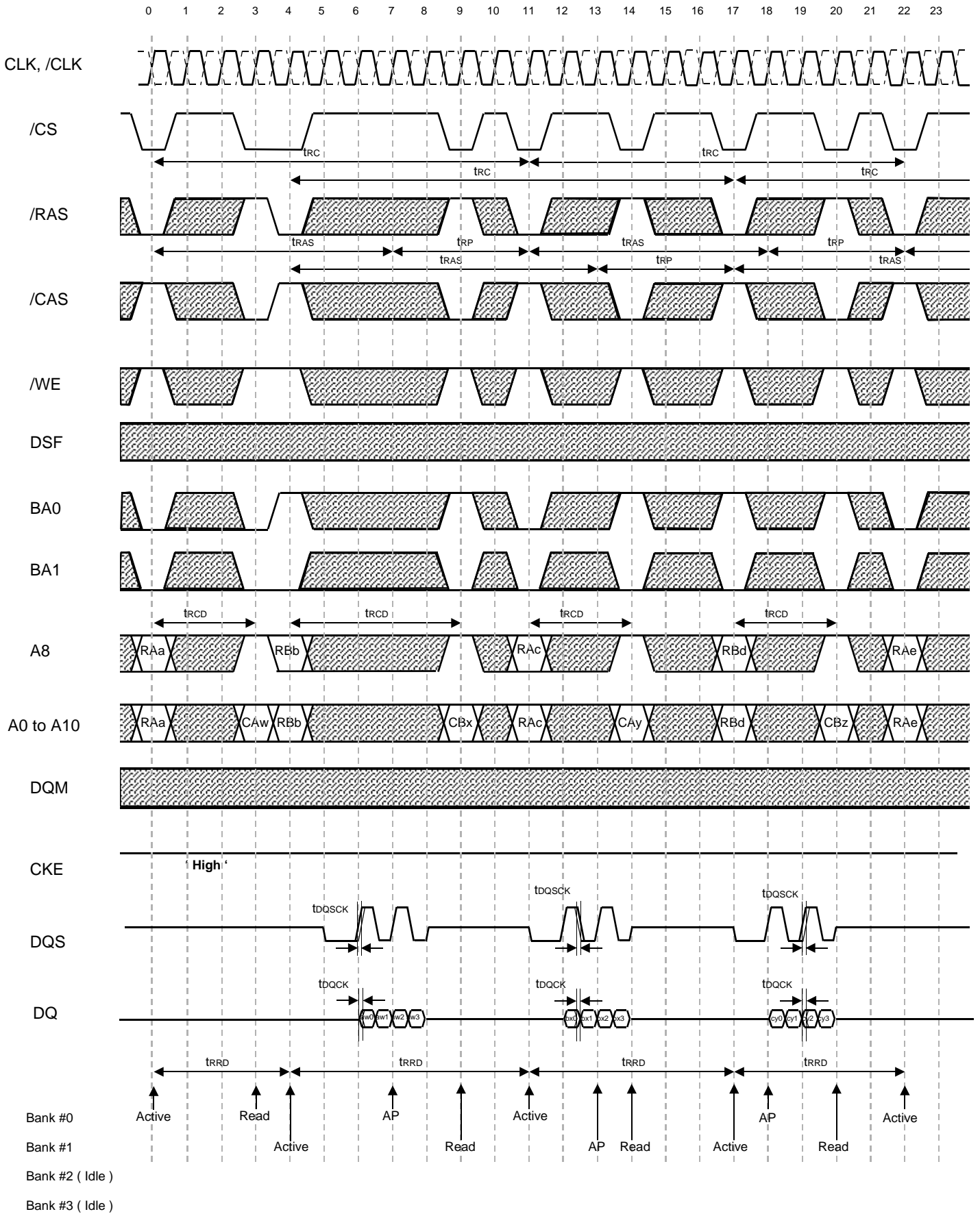
**Special Mode Register Set Cycle**



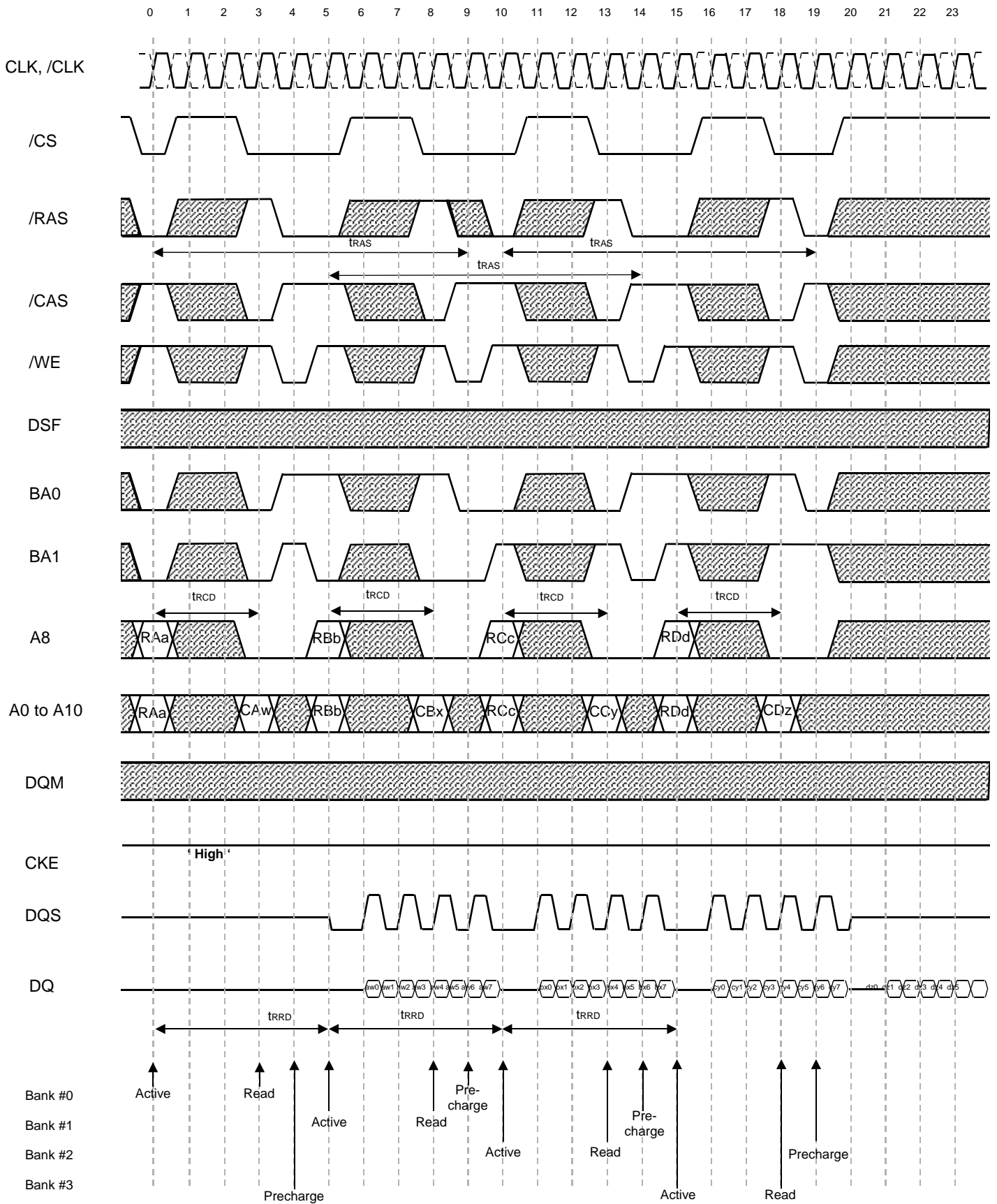
**Interleaved Bank Read ( BL=4, CL=3 )**



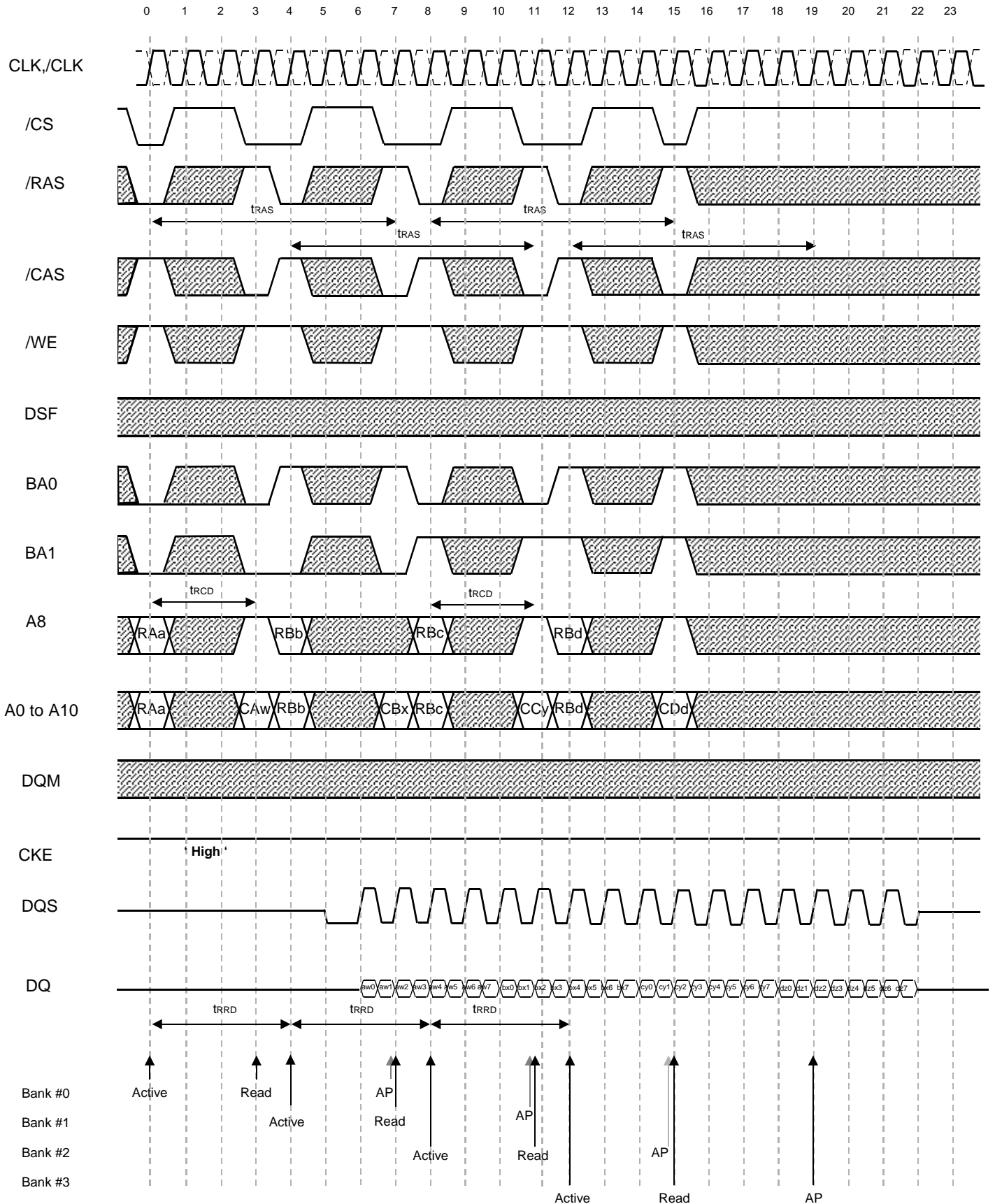
**Interleaved Bank Read ( BL=4, CL=3, Auto Precharge )**



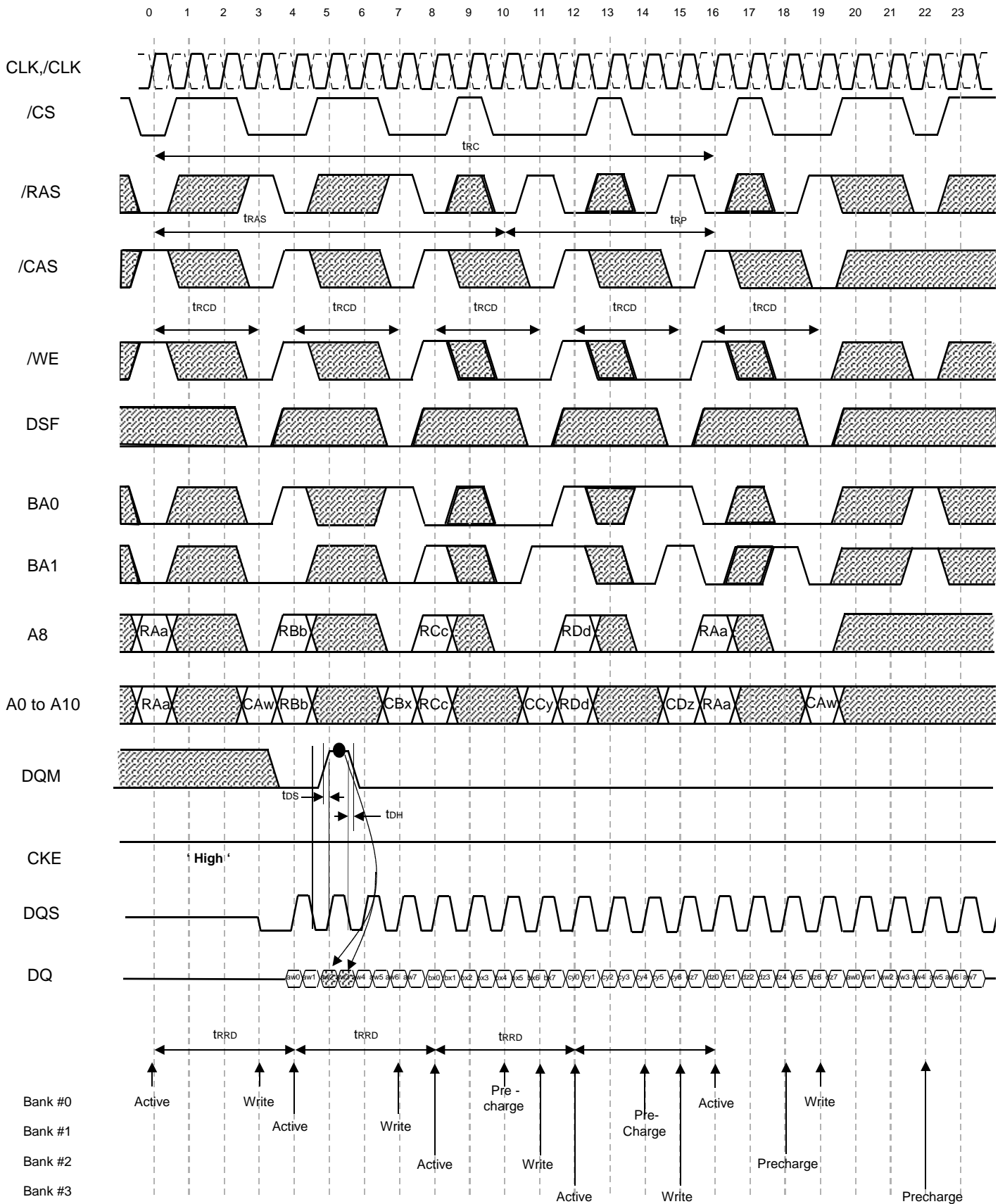
**Interleaved Bank Read ( BL=8, CL=3 )**



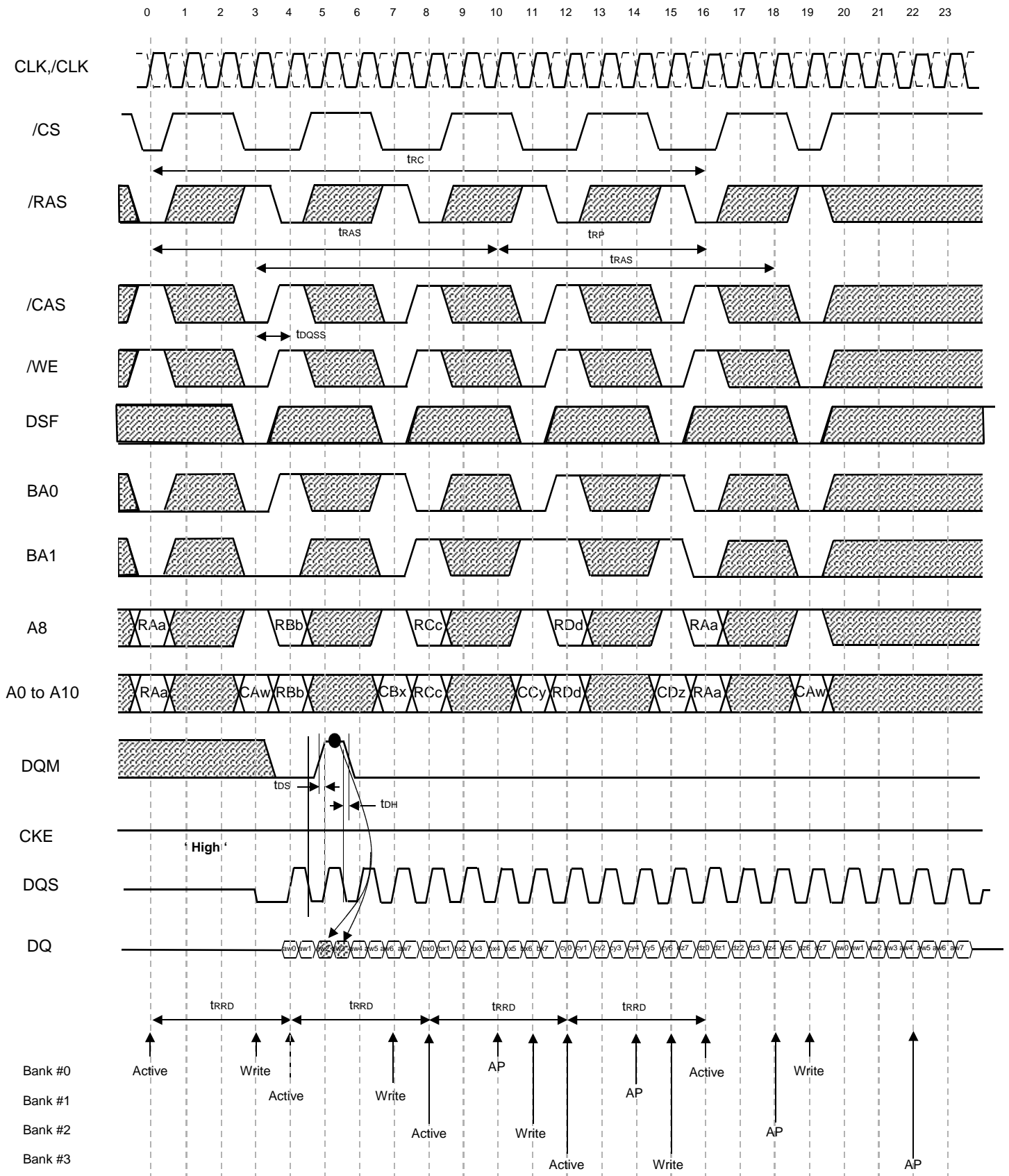
**Interleaved Bank Read ( BL=8, CL=3, Auto Precharge )**



**Interleaved Bank Write ( BL=8 )**

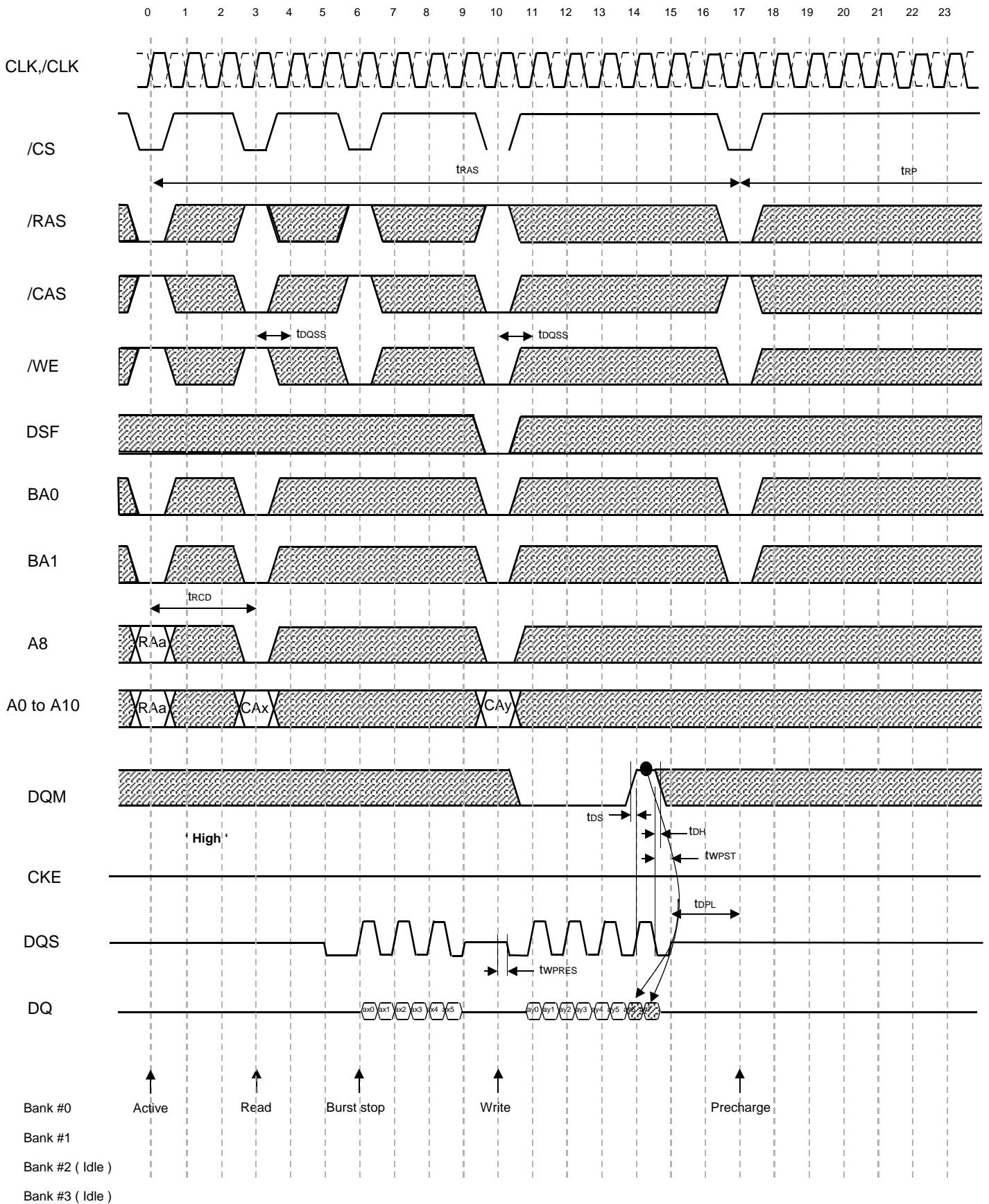


**Interleaved Bank Write ( BL=8, Auto Precharge )**

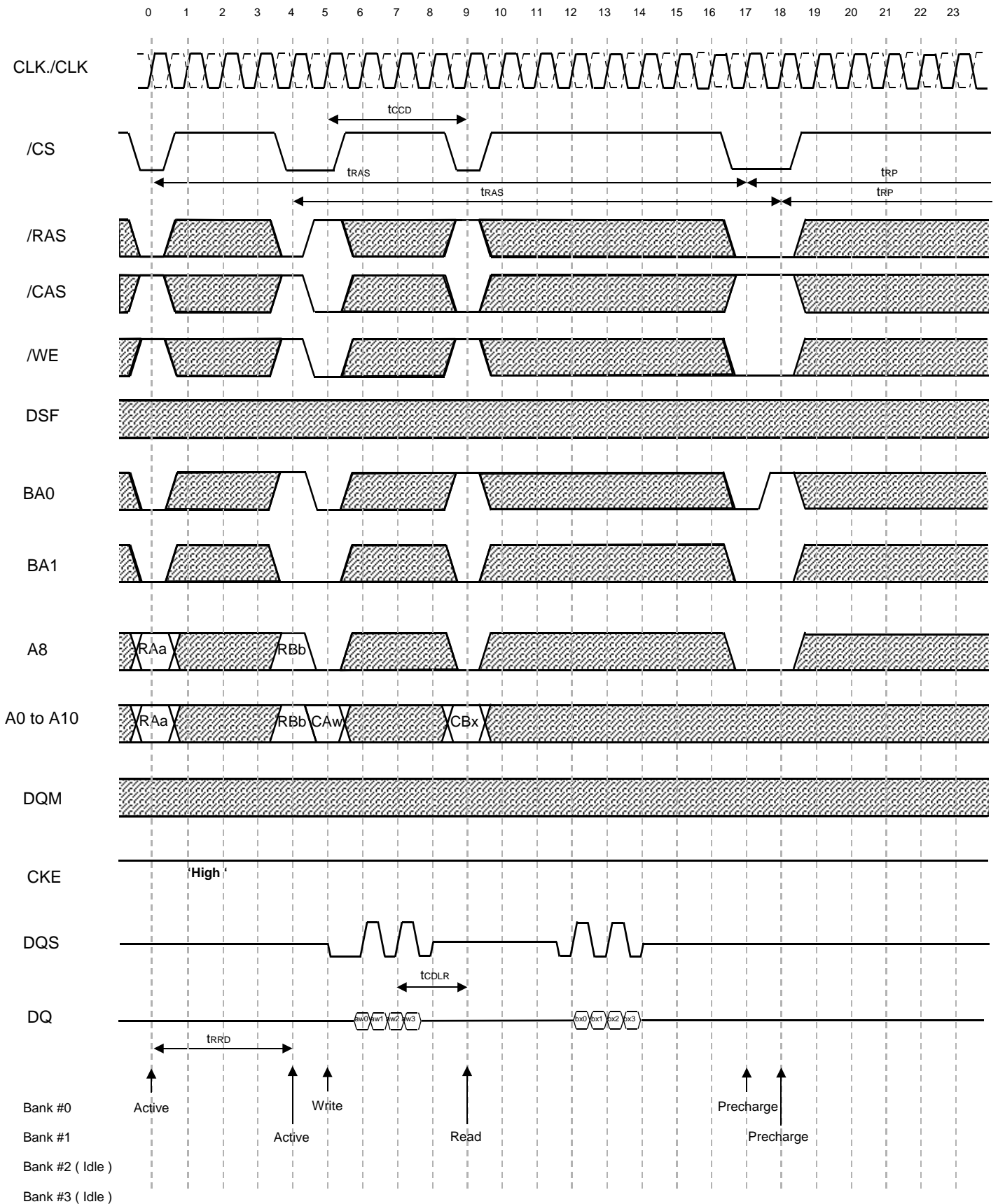




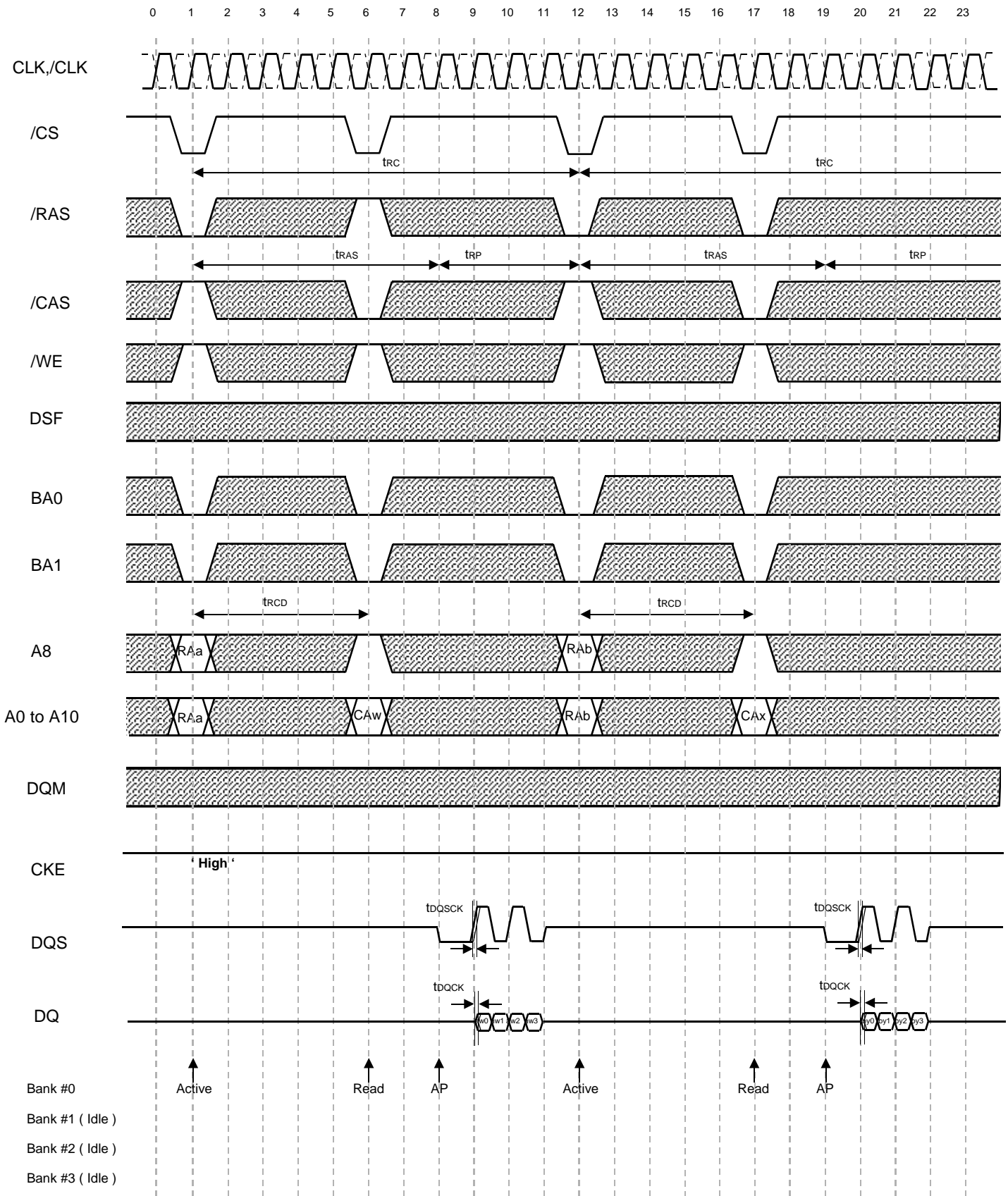
**Page Mode Read / Write ( BL=8, CL=3 )**



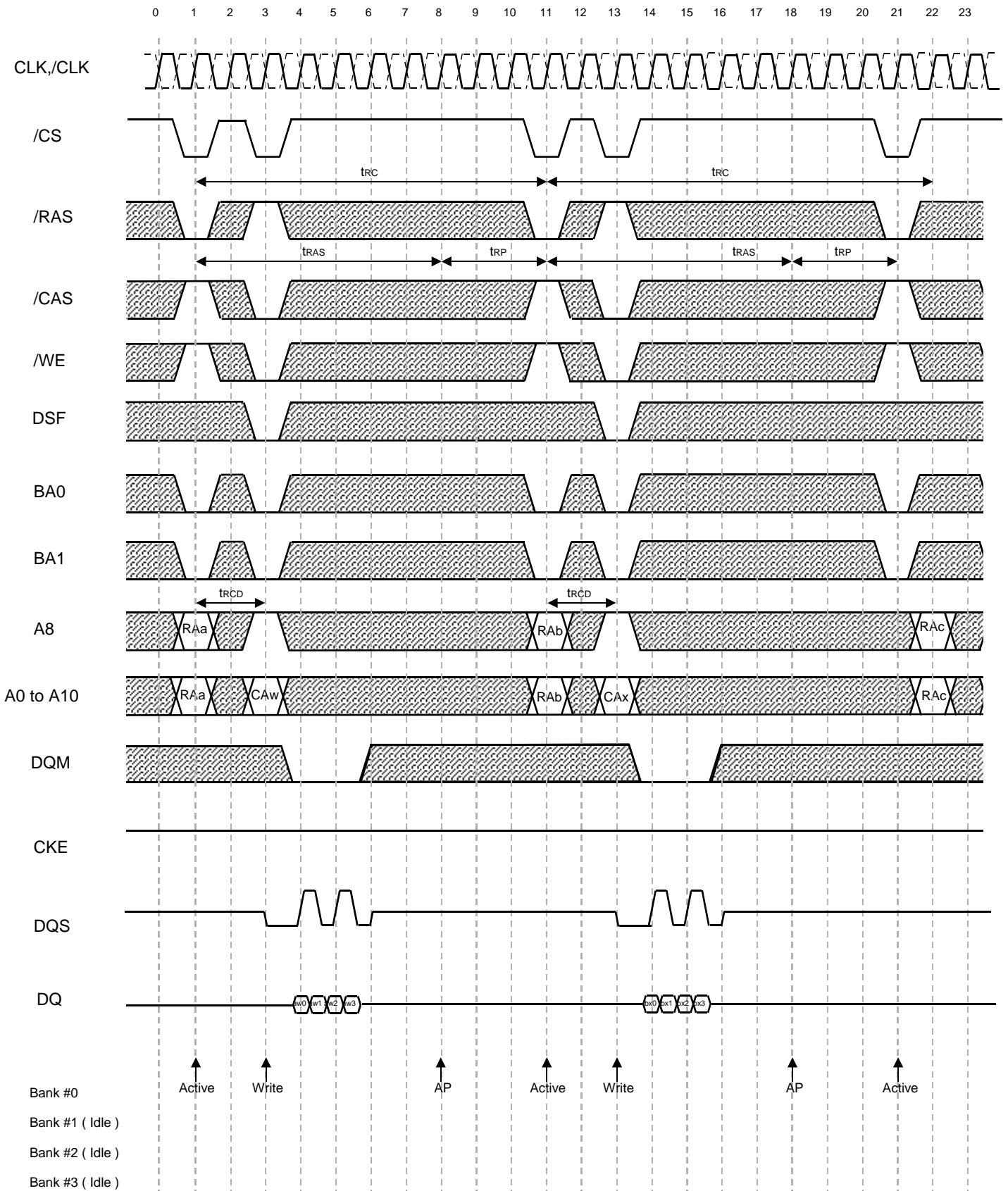
**Page Mode Write/Read ( BL = 4, CL = 3 )**



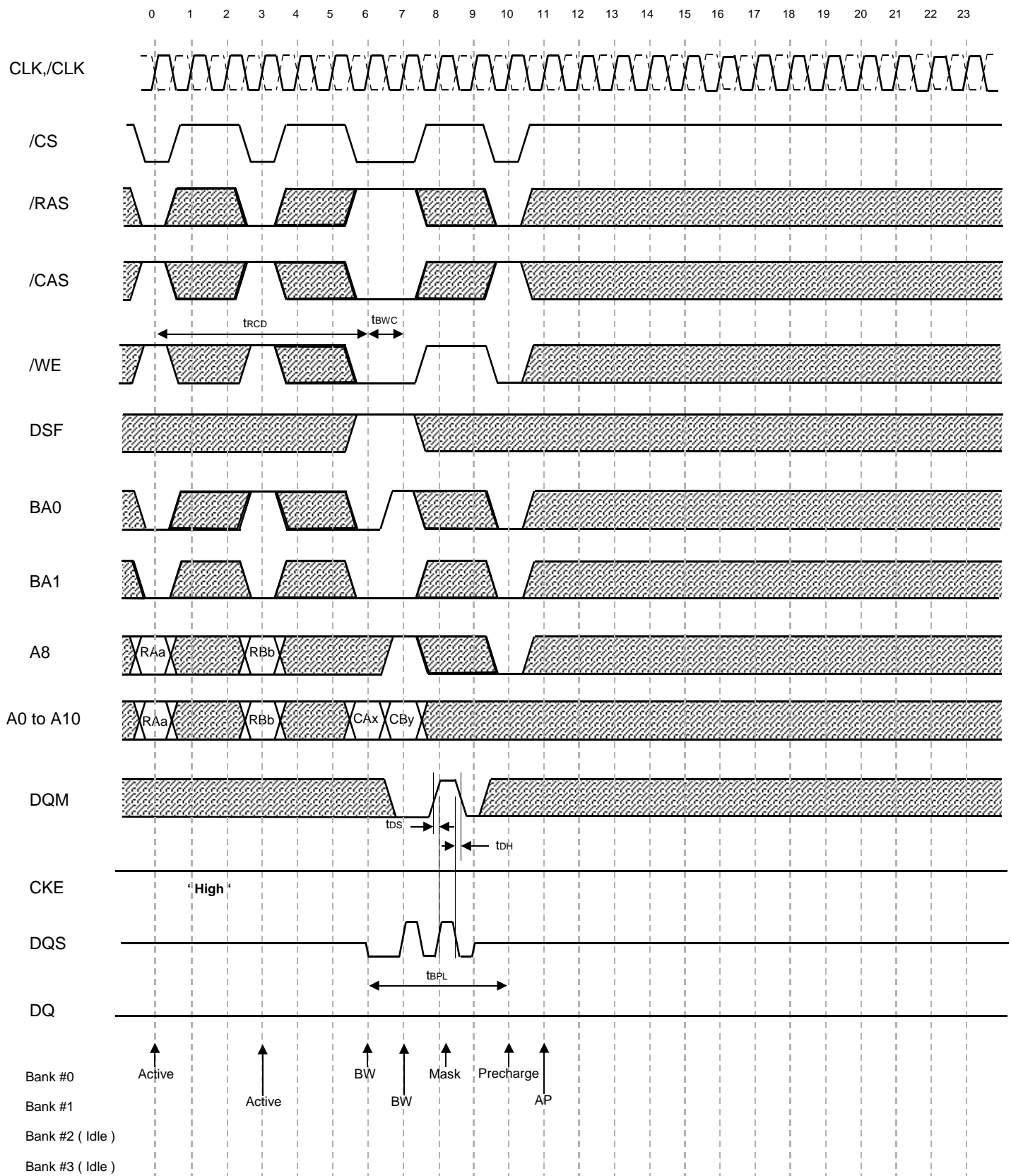
**Auto Precharge Read ( BL=4, CL=3)**



**Auto Precharge Write ( BL=4 )**

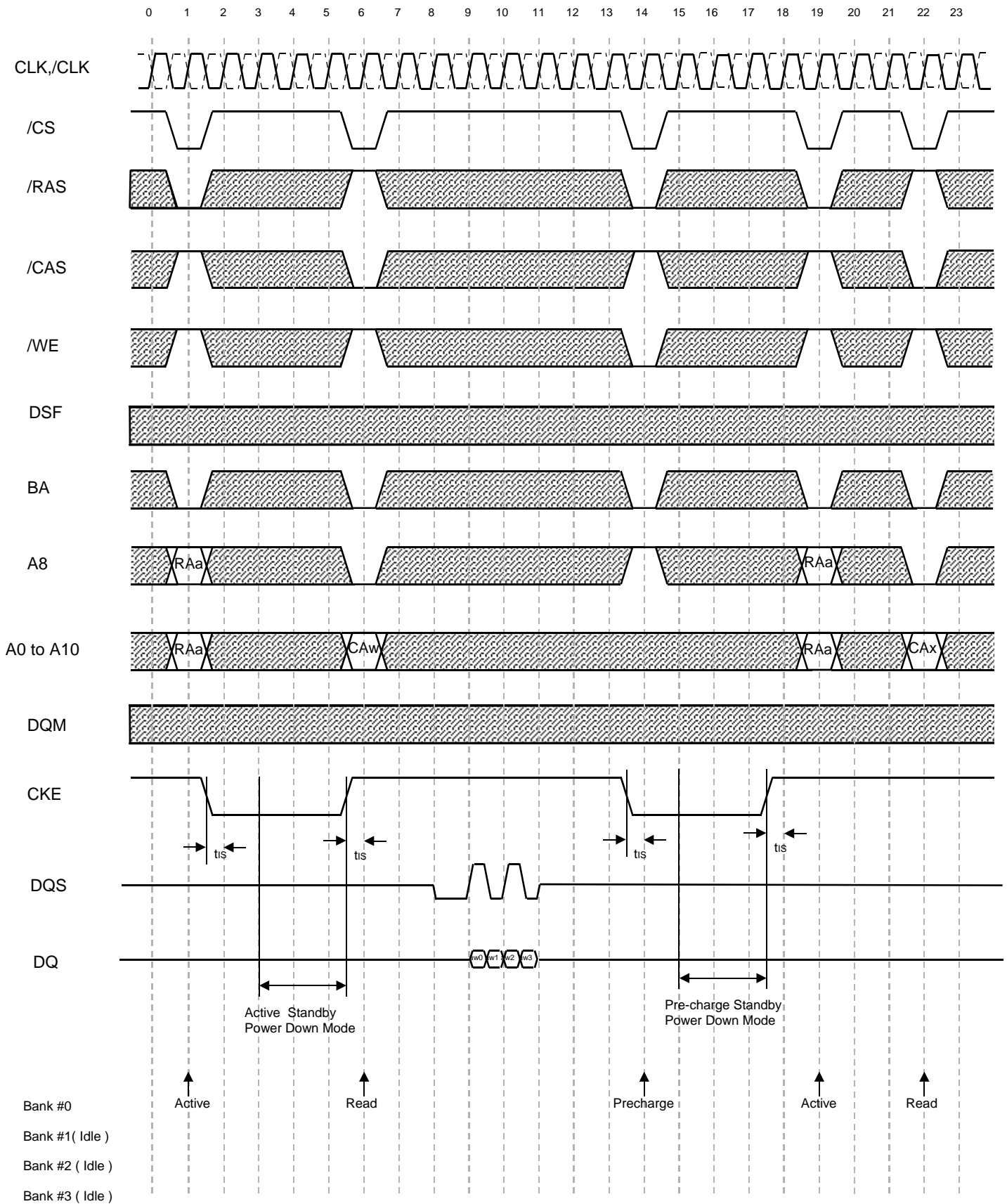


### Block Write ( Auto Precharge )

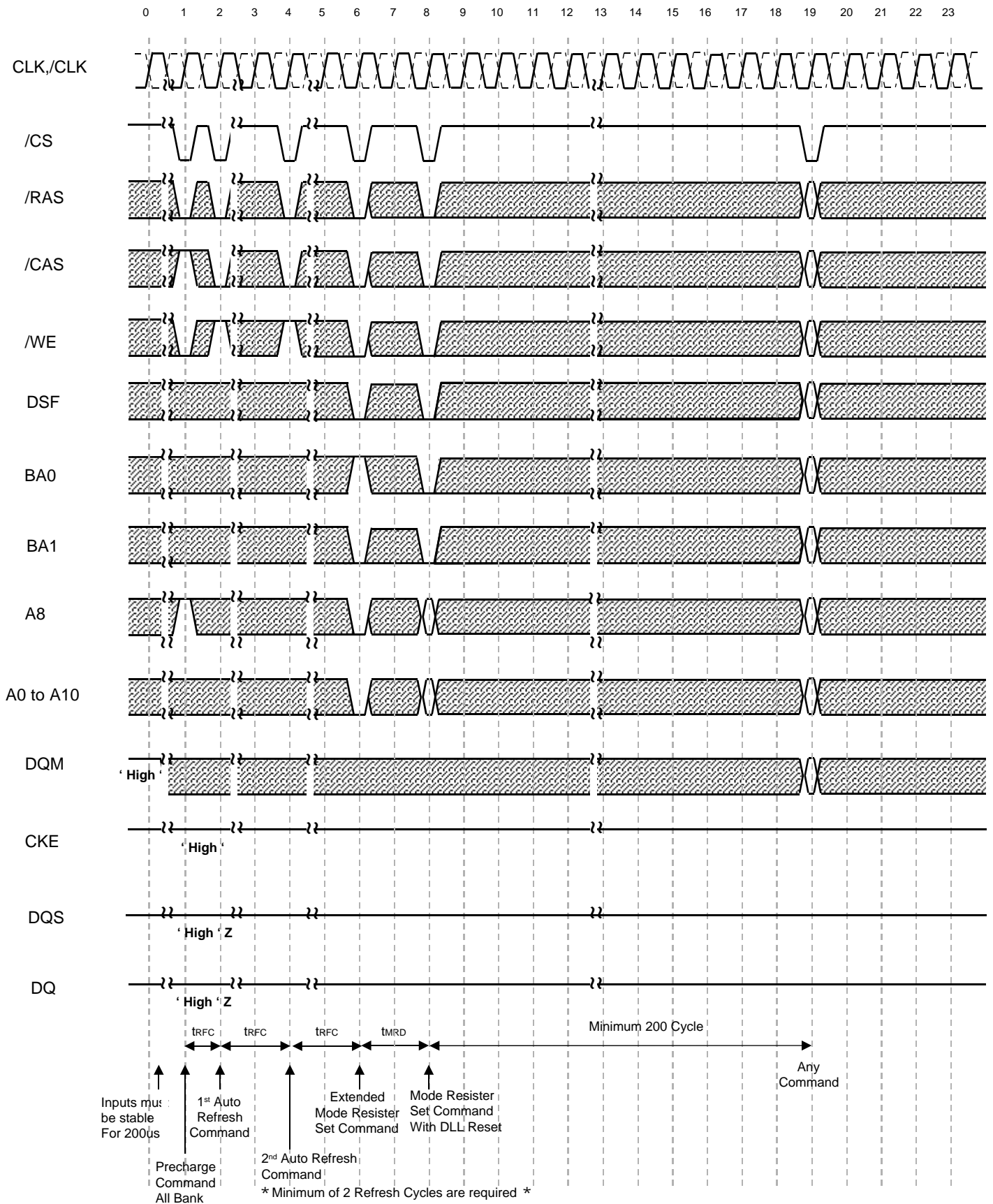


Note: Column address 0-3 are ignored

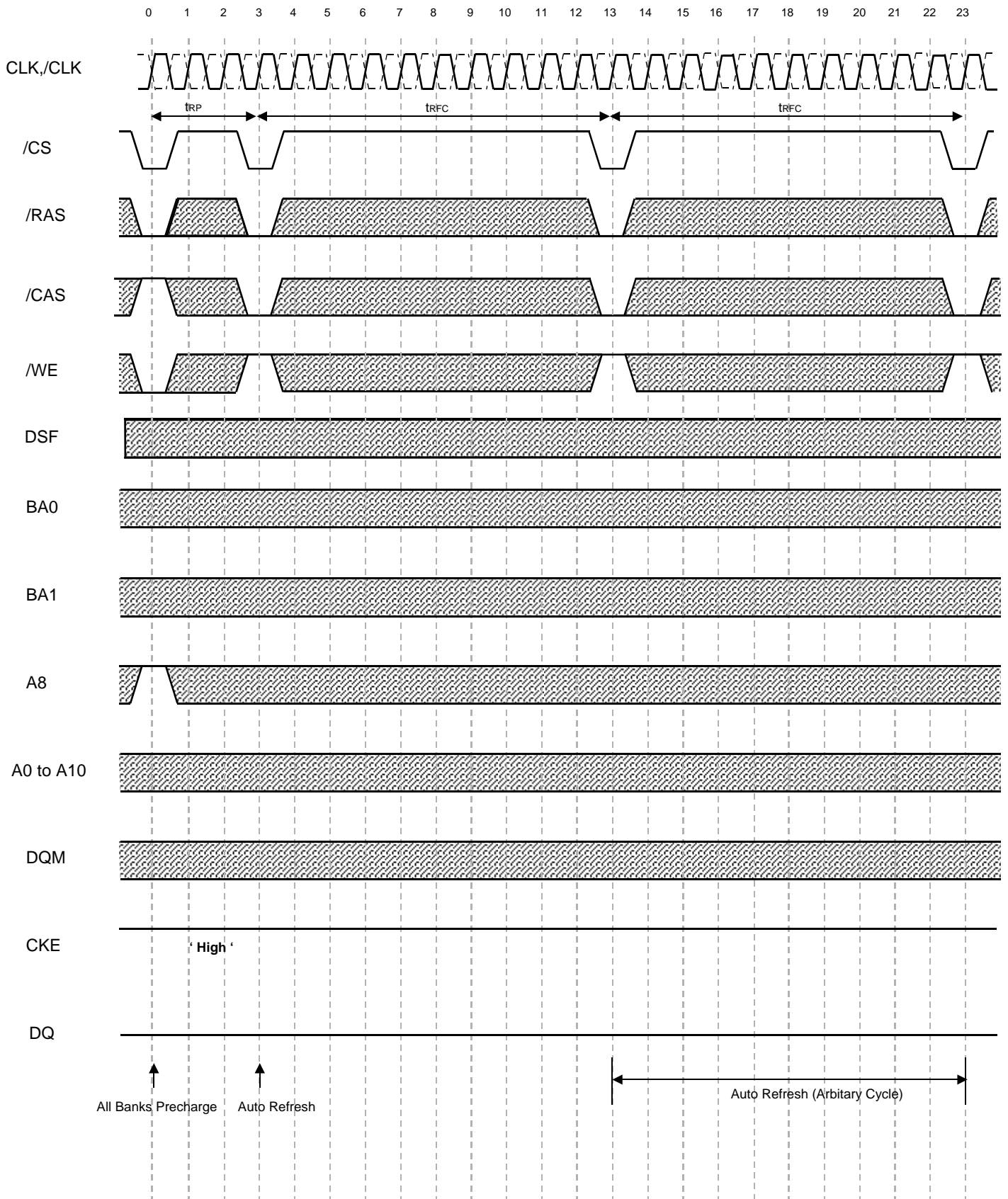
**Power Down Mode**



### Power Up Sequence & Auto Refresh (CBR)



### Auto Refresh Cycle





### Self Refresh Cycle

